

Serial No.: 09/625,996  
Docket No.: FIS9-2000-0138US1  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of

Docket No. FIS9-2000-0138US1

R. Dean Adams

Serial No.: 09/625,996

Group Art Unit: No.2133

Filed: July 26, 2000

Examiner: Chaudry, Mujtaba M.

For: **SYSTEM INITIALIZATION OF  
MICROCODE-BASED MEMORY BUILT-IN SELF-TEST**

Box: Final amendment  
Assistant Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**DRAFT**

**AMENDMENT UNDER 37 C.F.R. §1.116**

Sir:

In response to the Office Action dated April 8, 2003, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please amend claims 1 and 8 as follows. A clean copy of amended claims 1 and 8 is provided in the attached separate sheet, entitled "Clean Copy of Amended Claims."

- 1 Claim 1 (Twice Amended). An integrated circuit including an embedded memory and a
- 2 built-in self-test arrangement including
- 3 means for storing test instruction including means for discriminating a source of a
- 4 test command for performing manufacturing level and board level testing and receiving
- 5 test instructions provided from an external tester,
- 6 means for generating default test instructions, and
- 7 means for supplying said default test instructions to said means for storing test
- 8 instructions.